

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

my residence, post office address and citizenship are as stated below next to my name;

I verily believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **SEA-OF-CELLS ARRAY OF TRANSISTORS** (Docket No. 5161), the specification of which

X is attached hereto.

— was filed on _____ as
Application Serial No. _____
and was amended on _____.
(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:
None

Prior Foreign Application(s)		Priority Claimed		
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
_____	_____	_____	_____	_____

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior

application and the national or PCT international filing date of this application:
None

(Application Serial No.)(Filing Date) (Status) (Patented, Pending, Abandoned)

(Application Serial No.)(Filing Date) (Status) (Patented, Pending, Abandoned)

And I hereby appoint:

Douglas S. Foote of Dayton, Ohio, Registration No. 31,013,
and Stephen F. Jewett of Dayton, Ohio, Registration No. 27,565.

my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith. I hereby expressly waive my right to revoke the Power of Attorney granted above. Address all telephone calls to Douglas S. Foote at telephone number 513/445-3265. Address all correspondence to Douglas S. Foote, Intellectual Property Section, Law Department, NCR Corporation, World Headquarters, Dayton, Ohio 45479.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole inventor Harold S. Crans

Inventor's signature Harold S. Crans Date: 17/11/93

Residence 2575 Tamora Way, Colorado Springs, CO 80919

Citizenship United States of America

Post Office Address Same as above

Express Mail No. EV 325766564US
Date of Deposit: Nov. 21, 2003

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Address" service under 37 CFR on the date indicated above and is addressed to:

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PATENT

Attorney Docket No.: 000939-052180US

TOWNSEND and TOWNSEND and CREW LLP

By: 

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Harold S. Crafts

Application No.: TO BE ASSIGNED

Filed: HEREWITH

For: SEA-OF-CELLS ARRAY OF
TRANSISTORS

Examiner:

Art Unit:

APPOINTMENT OF ASSOCIATE
ATTORNEY UNDER 37 CFR § 1.34(b)

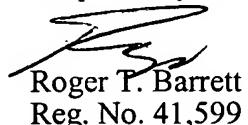
Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Under 37 CFR §1.34(b), applicant's undersigned principal attorney of record appoints **William F. Vobach, Registration No. 39,411; Chad E. King, Registration No. 44,187; Irvin E. Branch, Registration No. 42,358; and Thomas D. Franklin, Registration No. 43,616;**, as associate attorneys to prosecute this case and to transact all business in the Patent and Trademark Office connected therewith. The associate attorneys address and telephone number are as follows.

Phone: 303/571-4000
Address: Two Embarcadero Center, 8th Floor
San Francisco, California 94111-3834

Respectfully submitted,


Roger T. Barrett
Reg. No. 41,599

TOWNSEND and TOWNSEND and CREW LLP
Two Embarcadero Center, 8th Floor
San Francisco, California 94111-3834
Tel: (303) 471-4000 Fax: (303) 471-4321
WFV:klb
DE 60073175v1

"Express Mail" Label No. EV325766564US

Date of Deposit NOV. 21, 2003

PATENT

Attorney Docket No.: 000939-052180US

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Address" service under 37 CFR 1.10 on the date indicated above and is addressed to:

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

By: 

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Harold S. Crafts

Application No.: TO BE ASSIGNED

Filed: HEREWITH

For: SEA-OF-CELLS ARRAY OF
TRANSISTORS

Examiner:

Art Unit:

CHANGE OF POWER OF ATTORNEY
TRANSMITTAL

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

For the Examiner's information, attached hereto is a signed Revocation and Substitution of Power of Attorney Form under 37 C.F.R. §1.36, and a signed Certificate under 37 C.F.R. §3.73(b) for patent application no. 08/837,589 to which this application claims priority. In addition, because the signed copies of the documents are facsimile copies, and thus are hard to read, unsigned copies of the forms are enclosed to help the Examiner read and understand the signed copies.

It is not believed that the attached documents are strictly necessary in this application, since the undersigned firm is counsel of record in the parent case and the Application Data Sheet, filed herewith, includes correct representative and correspondence information. Nonetheless, since the original Declaration and Power of Attorney list a different

Application No.: TO BE ASSIGNED
000939-052160US
Page 2

PATENT

attorney and correspondence address, the documents are included herewith for clarity and to ensure that correspondence is directed to the correct address. If the Examiner has any questions about the Power of Attorney or the correct correspondence address, please direct correspondence to the undersigned at the address listed below or telephone the undersigned at (303) 571-4000.

Respectfully submitted,



William F. Vobach
Reg. No. 39,411

TOWNSEND and TOWNSEND and CREW LLP
Two Embarcadero Center, 8th Floor
San Francisco, California 94111-3834
Tel: (303) 571-4000 (in Denver)
Fax: (303) 571-4321
CEK/tnd
60073162 v1

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Assistant Commissioner for Patents,
Washington, D.C. 20231,
on

PATENT

TOWNSEND and TOWNSEND and CREW LLP

By _____

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
REVOCATION AND SUBSTITUTION OF POWER OF ATTORNEY
UNDER 37 CFR § 1.36

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 CFR § 1.36, Hyundai Electronics America, Inc. and NCR Corporation (formerly known as AT&T Global Information Solutions Company) revokes all previous powers of attorney and hereby appoints the following to prosecute the applications listed in the attached Schedule A and transact all business in the Patent and Trademark Office connected therewith:

Chad S. Hilyard, Reg. No. 40,647
Robert C. Colwell, Reg. No. 27,431
Darin J. Gibby, Reg. No. 38,464
Roger T. Barrett, Reg. No. 41,599

Please direct all future correspondence regarding the subject application to:

TOWNSEND and TOWNSEND and CREW LLP
Two Embarcadero Center, 8th Floor
San Francisco, CA 94111-3834

(303) 571-4000

By:

Halfred M. Hothery

Title:

Senior Vice President
and General Counsel
Hyundai Electronics

Date:

América, Inc.
10/12/98
f:\data\00939\US.Pat

By: Doug Foote

Doug Foote
Law Vice President
NCR Corporation

Date: 10/12/98

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Assistant Commissioner for Patents,
Washington, D.C. 20231,
on

PATENT

TOWNSEND and TOWNSEND and CREW LLP

By _____

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
REVOCATION AND SUBSTITUTION OF POWER OF ATTORNEY
UNDER 37 CFR § 1.36

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 CFR § 1.36, Hyundai Electronics America, Inc. and NCR Corporation (formerly known as AT&T Global Information Solutions Company) revokes all previous powers of attorney and hereby appoints the following to prosecute the applications listed in the attached Schedule A and transact all business in the Patent and Trademark Office connected therewith:

Chad S. Hilyard, Reg. No. 40,647
Robert C. Colwell, Reg. No. 27,431
Darin J. Gibby, Reg. No. 38,464
Roger T. Barrett, Reg. No. 41,599

Please direct all future correspondence regarding the subject application to:

TOWNSEND and TOWNSEND and CREW LLP
Two Embarcadero Center, 8th Floor
San Francisco, CA 94111-3834

(303) 571-4000

By:

Title: Halfred M. Hofherr
Senior Vice President
and General Counsel
Hyundai Electronics
America, Inc.

Date:

f:\data\00939s\US.Poa

By:

Title: Doug Foote
Law Vice President
NCR Corporation

Date:

CERTIFICATE UNDER 37 C.F.R. § 3.73(b)

Hyundai Electronics America, Inc., a California corporation and NCR Corporation, a Maryland corporation, certify that they are assignees of the patent applications identified on Schedule A by virtue of a chain of title from the inventor(s), of the patent application identified in Schedule A, to the current assignee(s) as shown below:

1. From: the Inventor(s) To: AT&T Global Information Solutions Company.
The document was recorded in the Patent and Trademark Office at
the Reel and Frame numbers identified on Schedule A.
2. ½ Interest From: AT&T Global Information Solutions Company To: Hyundai Electronics America, Inc.
The document was recorded in the Patent and Trademark Office at
Reel 7408 Frame 0104.
3. ½ Interest From: Hyundai Electronics America, Inc. To: Symbios Logic Inc.
The document was recorded in the Patent and Trademark Office at
Reel 7679 Frame 0431.
4. Name Change From: Symbios Logic Inc. to Symbios Inc.
The document was recorded in the Patent and Trademark Office at
Reel 9089 Frame 0936.
5. ½ Interest From: Symbios Inc. To: Hyundai Electronics America, Inc.
A copy of the assignment document is attached.

The undersigned (whose titles are supplied below) are each empowered to act on behalf of the assignees.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date : 10/12/88

Name : Halfred M. Hofner

Title : Senior Vice President and General Counsel, Hyundai Electronics America, Inc.

Signature : Halfred M. Hofner

Date : _____

Name : Doug Root

Title : Law Vice President, NCR Corporation (Formerly known as AT&T Global Information Solutions Company)

Signature : Doug Root

CERTIFICATE UNDER 37 C.F.R. § 3.73(b)

Hyundai Electronics America, Inc., a California corporation and NCR Corporation, a Maryland corporation, certify that they are assignees of the patent applications identified on Schedule A by virtue of a chain of title from the inventor(s), of the patent application identified in Schedule A, to the current assignee(s) as shown below:

1. From: the Inventor(s) To: AT&T Global Information Solutions Company.
The documents were recorded in the Patent and Trademark Office at
the Reel and Frame numbers identified on Schedule A.
2. $\frac{1}{2}$ Interest From: AT&T Global Information Solutions Company To: Hyundai Electronics America, Inc..
The document was recorded in the Patent and Trademark Office at
Reel 7408, Frame 0104.
3. $\frac{1}{4}$ Interest From: Hyundai Electronics America, Inc. To: Symbios Logic Inc..
The document was recorded in the Patent and Trademark Office at
Reel 7629, Frame 0431.
4. Name Change From: Symbios Logic, Inc. to Symbios, Inc..
The document was recorded in the Patent and Trademark Office at
Reel 9089, Frame 0936.
5. $\frac{1}{4}$ Interest From: Symbios, Inc. To: Hyundai Electronics America, Inc..
A copy of the assignment document is attached.

The undersigned (whose titles are supplied below) are each empowered to act on behalf of the assignees.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date : _____

Name : Halfred M. Hofherr

Title : Senior Vice President and General Counsel, Hyundai Electronics America, Inc.

Signature : _____

Date : _____

Name : Doug Foote

Title : Law Vice President, NCR Corporation (Formerly known as AT&T Global Information
Solutions Company)

Signature : _____

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

my residence, post office address and citizenship are as stated below next to my name;

I verily believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: SEA-OF-CELLS ARRAY OF TRANSISTORS (Docket No. 5161), the specification of which

X is attached hereto.

____ was filed on _____ as
Application Serial No. _____
and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:
None

Prior Foreign Application(s) Priority Claimed

(Number)	(Country)	(Day/Month/Year Filed)	Yes	No

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior

PCT international filing date application:
None

(Application Serial No.)(Filing Date) (Status) (Patented, Pending, Abandoned)

(Application Serial No.)(Filing Date) (Status) (Patented, Pending, Abandoned)

And I hereby appoint:

Douglas S. Foote of Dayton, Ohio, Registration No. 31,013,
and Stephen F. Jewett of Dayton, Ohio, Registration No. 27,565.

my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith. I hereby expressly waive my right to revoke the Power of Attorney granted above. Address all telephone calls to Douglas S. Foote at telephone number 513/445-3265. Address all correspondence to Douglas S. Foote, Intellectual Property Section, Law Department, NCR Corporation, World Headquarters, Dayton, Ohio 45479.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole inventor Harold S. Craft

Inventor's signature HS Craft

Date: 7/11/83

Residence 2575 Tamora Way, Colorado Springs, CO 80919

Citizenship United States of America

Post Office Address Same as above

SCHEDULE A

File No. 00939S-	App. No.	Filing Date	Title	Examiner	Art Unit	Inventor Assignment Recorded At: Reel_, Frame_
049700US	08/905,922	08/05/97	Digitizer Stylus Containing Handwriting Data	Monica S. Davis	2724	7301, 0299
049800US	08/243,679	05/16/94	Method and Apparatus for Regulation of Power Dissipation	T. Lam	2502	7006, 0079
049900US	08/929,841	09/15/97	Shallow Trench Isolation in Integrated Circuits	G. Fourson, III	1107	6864, 0225
050000US	08/828,246	03/31/97	Electronic Discharge Protection System for Mixed Voltage Application Specific Integrated Circuit Design	Ronald Leja	2836	9089, 0936
050100US	08/826,506	04/11/97	Electrostatic Pen Apparatus and Method Having an Electrically Conductive and Flexible Tip	P. Loomis	2743	7315, 0461

File No. 00939S-	App. No.	Filing Date	Title	Examiner	Art Unit	Inventor Assignment Recorded At: Reel_, Frame_
0502000US	08/712,826	09/12/96	Wall Outlet with Direct Current Output	S. Hecker	2111	7233, 0503
0503000US	08/783,782	01/15/97	FIFO Status Indicator	Madelein Nguyen	2714	6841, 0916
0504000US	08/792,821	01/30/97	Movie-On-Demand Disk Storage Loop Architecture	David Vincent	2615	7110, 0011
0505000US	08/299,395	09/01/94	Integrated Circuit I/O Pad Cell Modeling	Jacques J. Louis	3614	7135, 0100
0506000US	08/834,154	04/14/97	Single-Edge Triggered Phase Detector	A. Kinkhead	2502	6824, 0524
0507000US	08/829,745	03/31/97	Integrated Circuit Device with Reduced Cross Talk	P. Brown	2508	6810, 0332
0508000US	08/286,720	08/05/94	Compensation of Stylus Signals in Digitizing Tablet	V. Shankar	2605	7106, 0596
0509000US	08/191,908	02/04/94	High Voltage Stylus for Portable Computer	K. Kim	2614	6941, 0133

File No. 00939S-	App. No.	Filing Date	Title	Examiner	Art Unit	Inventor Assignment Recorded At: Reel_, Frame_
050910US	08/818,615	03/14/97	High Voltage Stylus for Portable Computers	K. Kim	2614	6941, 0133
051000US	08/254,022	06/03/94	Tunable Architecture for Device Adapter	P. S. Lai	2315	7042, 0021
051100US	07/935,301	08/26/92	Semiconductor Fuse with Polysilicon Plate and Method of Fabrication	H. Tsai	1107	6210, 0772
051110US	08/469,942	06/06/95	Semiconductor Fuse with Polysilicon Plate and Method of Manufacturing	J. Carroll	2508	6210, 0772
051200US	08/357,944	12/16/94	Integrated Image Sensor Array for Electronic Camera	N. Vu	2712	7283, 0793
051300US	08/839,426	04/14/97	Semiconductor Fuse Structure	R. Booth	1107	6006, 0236
051400US	08/567,504	12/05/95	Global Planarization Using SOG & CMP	L. Fleck	1109	6320, 0766

File No. 00939S-	App. No.	Filing Date	Title	Examiner	Art Unit	Inventor Assignment Recorded At: Reel_, Frame_
051500US	08/432,404	05/01/95	Current Limited Output Driver for a Gate Array Circuit	M. Wambach	2504	6380, 0136
051600US	08/976,683	11/24/97	Intelligent SCSI-2/DMA Processor	H.T. Nguyen	2751	6156, 0167
051700US	08/729,193	10/15/96	Centralized Management of Resources Shared by Multiple Processing Units	P. Myers	2305	7004, 0896
051800US	08/352,401	12/08/94	Image Processing Apparatus Including Horizontal & Vertical Scaling for a Computer Display	K.M. Tong	2415	7279, 0884
051900US	08/725,913	10/04/96	Supply Voltage Tolerant Phase Locked Loop Circuit	S. Grimm	2502	7181, 0445
052000US	08/906,254	08/04/97	Telemetry by Digitizer Stylus	P. Loomis	2743	7385, 0754

File No. 00939S-	App. No.	Filing Date	Title	Examiner	Art Unit	Inventor Assignment Recorded At: Reel_, Frame_
052100US	08/543,335	10/16/95	Sea-Of-Cells Array of Transistors	W. Fahmy	2508	6843, 0255
052110US	08/837,589	04/21/97	Sea-Of-Cells Array of Transistors	V. Sick	2784	6843, 0255
052200US	08/082,862	06/25/93	Method & Apparatus for Pseudo-Aligned Transfers of Data to Memory	D. Chen	2317	6592, 0947
052300US	07/962,544	10/19/92	Pillar Emitter for BiCMOS Devices	D. Ostrowski	2814	6377, 0811
052400US	08/258,357	06/09/94	Multiple Channel Data Bus Routing Switching Including Parity Generation	D. Nguyen	2302	5716, 0450
052500US	08/119,295	09/10/93	Dual Mode Graphics Controller with Preemptive Video Access	U. Chauhan	2415	6695, 0115
052600US	08/625,986	04/01/96	Field Implant for Silicon Controlled Rectifier	S. Loke	2508	6825, 0614

File No. 00939S-	App. No.	Filing Date	Title	Examiner	Art Unit	Inventor Assignment Recorded At: Reel , Frame _
052700US	08/097,183	07/27/93	Bonding of Silicon Wafers	V. Wallace	2503	6647, 0090
052710US	08/458,592	06/02/95	Bonding of Silicon Wafers	M. Trish "	1104	6647, 0090
052800US	08/192,078	02/04/94	Cancellation of Common- Mode Signals in Digitizing Tablet	K. Kim	2614	6941, 0615
052900US	08/586,365	01/16/96	Bi-CMOS Integrated Circuit	W. Mintel	2508	6626, 0013
052910US	08/866,968	06/02/97	Bi-CMOS Integrated Circuit		1104	6626, 0013
052920US	08/934,239	09/19/97	Bi-CMOS Integrated Circuit	S. V. Clark	2815	6413, 0953
053000US	08/570,256	12/11/95	Architecture & Configuring Method for a Computer Expansion Board	K. Ellis	2312	5185, 0768
053100US	08/065,387	05/20/93	Step Addressing in Video Ram	R. Hjerpe	2609	6681, 0924

File No. 00939S-	App. No.	Filing Date	Title	Examiner	Art Unit	Inventor Assignment Recorded At: Reel_, Frame_
053200US	08/971,464	11/17/97	Method & Apparatus for Polymer Coating of Substrates	C. Padgett	1112	7258, 0608
053300US	08/354,574	12/12/94	Bipolar Silicon-on-Insulator Structure	V. Wallace	2503	7263, 0502
053310US	08/748,969	11/13/96	Bipolar Silicon-on-Insulator Structure & Process	T. Nguyen	1104	7263, 0572
053400US	08/738,916	10/28/96	Negative Voltage Generator for Use with N-Well CMOS Process	T. Cunningham	2504	6872, 0529
053500US	08/026,669	03/05/93	Automated Washing System & Method	S. Vincent	1303	6455, 0956
053600US	07/101,452	09/28/87	Architectural Arrangement for a SCSI Disk Controller Integrated Circuit	G. Ray	237	4793, 0433
056800US	08/159,981	11/30/93	Planarization of Integrated Circuits	M. Whipple	1104	6885, 0686

App. No.	Filing Date	Title	Examiner	Art Unit	Inventor Assignment Recorded At: Reel , Frame _
00939S-					
064100US	08/355,791	12/14/94 Method for Cell Swapping to Improve Pre-Layout to Post Layout Timing	T. Walker	2063	7298, 0811